

ECED 3204 Microprocessor

Assignment #1

<http://www.jasongu.org/3204/assignments.html>

Due date: September 20 2017. Late submission will not be accepted.

Assignment #1 contains the following problems:

E2.1 Write an instruction sequence to swap the contents of registers r0 and r1. A third register such as r2 or other can be used to facilitate the swap operation.

E2.4 Write an instruction sequence to store 1, 3, and 5 at data memory locations at 0x2000, 0x2005, and 0x200A, respectively.

E2.7 Write a sequence of instructions to add the three numbers stored in 0x2000, 0x2001, and 0x2002 and store the sum at data memory location at 0x2010.

E2.10 Write a sequence of instructions to copy the three bytes from program memory locations 0x2000~0x2002 to data memory locations at 0x2000~0x2002, respectively.

E2.13 Write an instruction sequence to subtract the value stored at data memory location 0x2000 from the sum of the values stored at data memory locations 0x2010 and 0x2011 and leave the difference in r0.